



Fiche signalétique / Identification sheet

DONNÉES PERSONNELLES / PERSONAL INFORMATION

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	Gr. thématique / Theme	Capteurs, actionneurs et conditionnement
	Institut / Institute	Institut Systèmes industriels
	Fonction / Function	Adjoint scientifique

COMPÉTENCES SPÉCIFIQUES

- ▶ Analog and Digital Signal Processing
- ▶ VHDL
- ▶ Low noise, high-resolution data acquisition

DOMAINES DE RECHERCHE

- ▶ Low noise, high-resolution data acquisition

TRAVAUX EXEMPLAIRES

- ▶ SMA, Small microphone array. – Development of a HW platform linking a set of 8 microphone inputs and as many loudspeaker or line outputs to high performance DSPs (ADSP TS101S EZ-KIT Lite). In particular, development of custom FPGA board « Digital I/F board », interfacing the DSP Linkports to the codecs and the firewire interfaces.
- ▶ Development of low frequency, low noise, high-resolution (24 bits) data acquisition systems for different space applications, including implementation of specific digital signal processing in FPGAs.