



DESIM, VHDL SIMULATION ENGINE

Simulation engine for distributed testbenches

Project Summary

The aim of this project was to set-up a test methodology based on an engine driving a Device Under Test (DUT) based on command files. The engine is to remain the same for different DUTs and only DUT-specific adapters will have to be developed in order to cope with a new DUT interface.

The test results are compiled in order to provide more or less detailed information according to the interest of the user. This work is done by the «Global Simulation Script».

A further objective was to allow the partition of a longer simulation task over several machines, parallelizing the simulation and thus reducing the simulation time.

Valorisation

The project results have been used to simulate GRLib-based designs <www.gaisler.com> for space applications.

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